

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Brendan K. Bridgford  
Assignee: Xilinx, Inc.  
Title: "A Method of Debugging PLD Configuration Using Boundary Scan"  
Serial No.: 10/606,728 Filing Date: 06/26/2003  
Examiner: Amine Riad Art Unit: 2113  
Docket No.: X-1216 US Conf. No.: 5830

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RESPONSE TO FIRST OFFICE ACTION AFTER A SECOND RCE

In response to the First Office Action after a second RCE mailed from the Patent Office on November 28, 2007, Applicant submits the following amendments and remarks.

**The Listing of the Claims** begin on page 2 of this paper.

**Remarks** begin on page 7 of this paper.